

46. (New) A method of manufacturing a memory module according to claim 45, wherein each of the first semiconductor devices is a DRAM device.

47. (New) A method of manufacturing a memory module according to claim 46, wherein the second semiconductor device is a nonvolatile read-only memory device.

48. (New) A method of manufacturing a memory module according to claim 46, wherein the second semiconductor device is an EEPROM device.

REMARKS

The Applicants request entry of the foregoing amendments and new claims.

Claims 25-48 are now pending and presented for examination.

Respectfully submitted,



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